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# SHARC

## Super Harvard Architecture

**SHARC** (Analog Devices) **DSP** .  
 CPU , 1000 CPU <sup>1)</sup>

- SMP(Symmetric Multiprocessing) SHARC

- , / 가

<sup>2)</sup> 32 VLIW :

- : (48 ), / (32 ), (40 )
- : 가 ( : 48 + 32 )
- : 가 <sup>3)</sup>

- :
  1. 32 가
  2. ( `if condition` )
  3. 2 <sup>4)</sup>

- :
  1. 6 <sup>5)</sup>

- :
  1. 2
  2. 가 <sup>6)</sup>

- **DMA** :
  - 1.

- **32** : 16GB(32 ), 20GB(40 ), 24GB(48 )

- : 40

- :
- :
- :

### DSP

1)

1994 1 가

2)

3)

32 /

4)

5)

6)

OS/



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