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3가 가 .

가 가 .

# Electronically balanced

## Active buffer Output

Op-amp , FET  
splitter.

+ -

Active

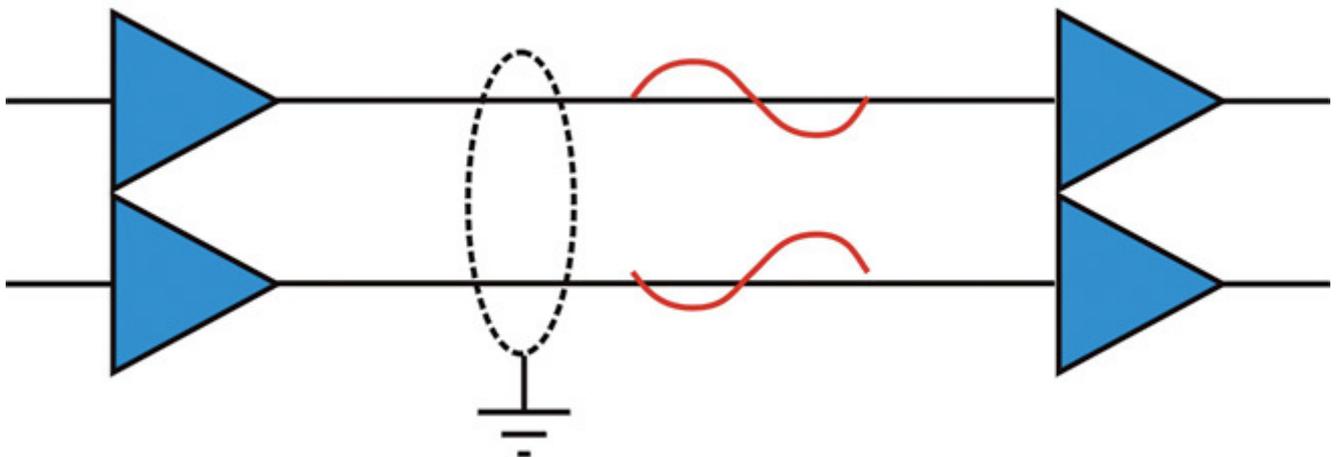
CMRR

가 CMRR 가 가 , CMRR 가 .

DC

가 가

1)



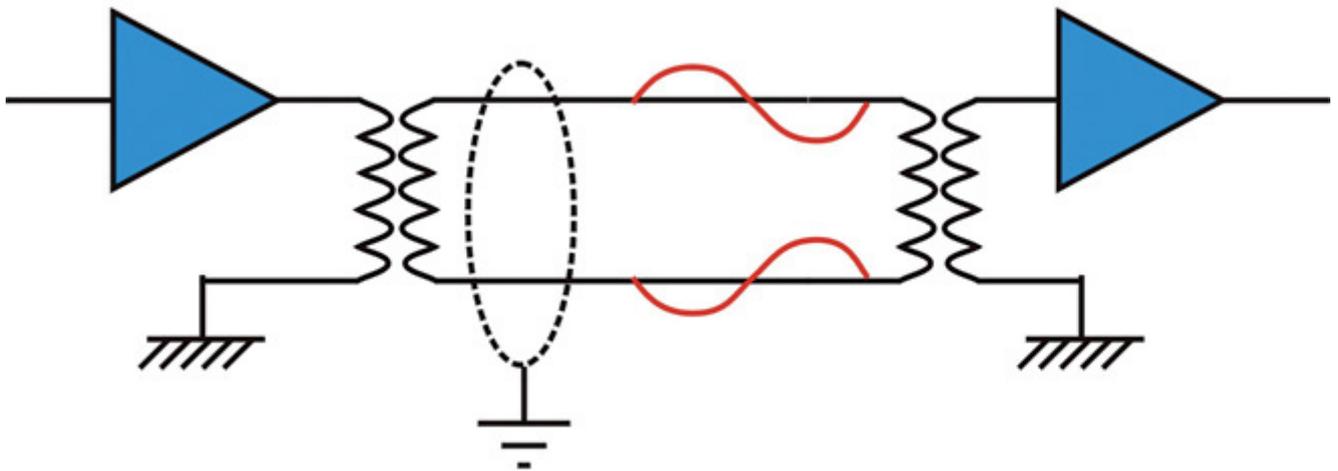
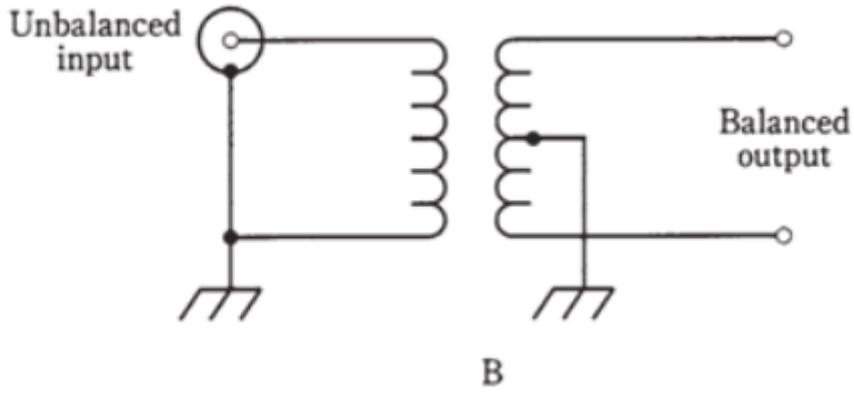
# Transformer balanced

## Transformer Output

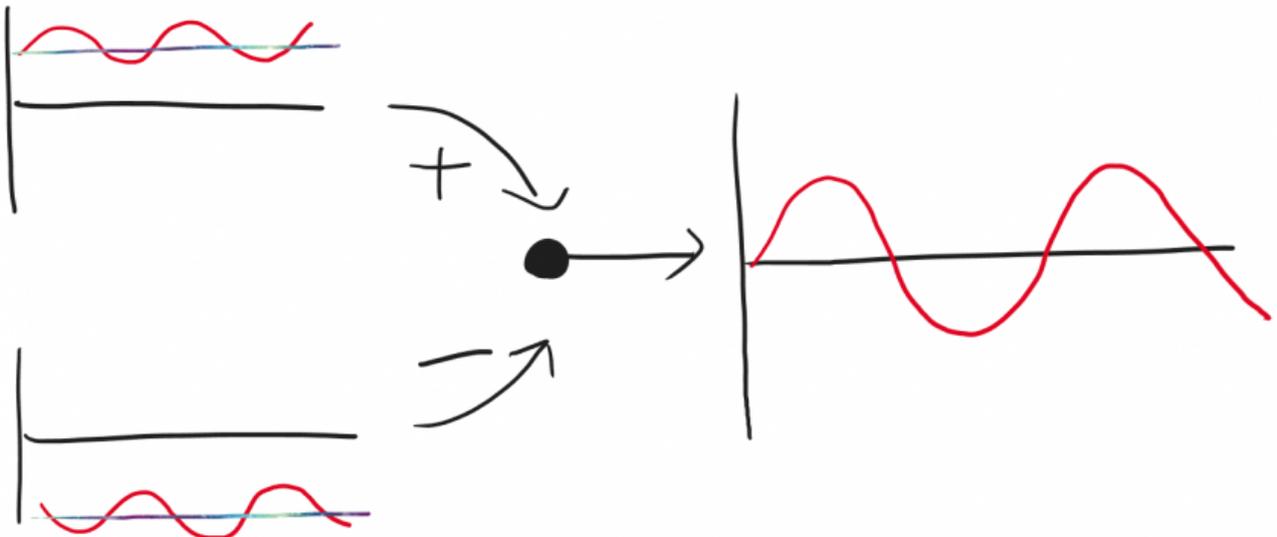
+ -

Passive

splitter.



DC



가

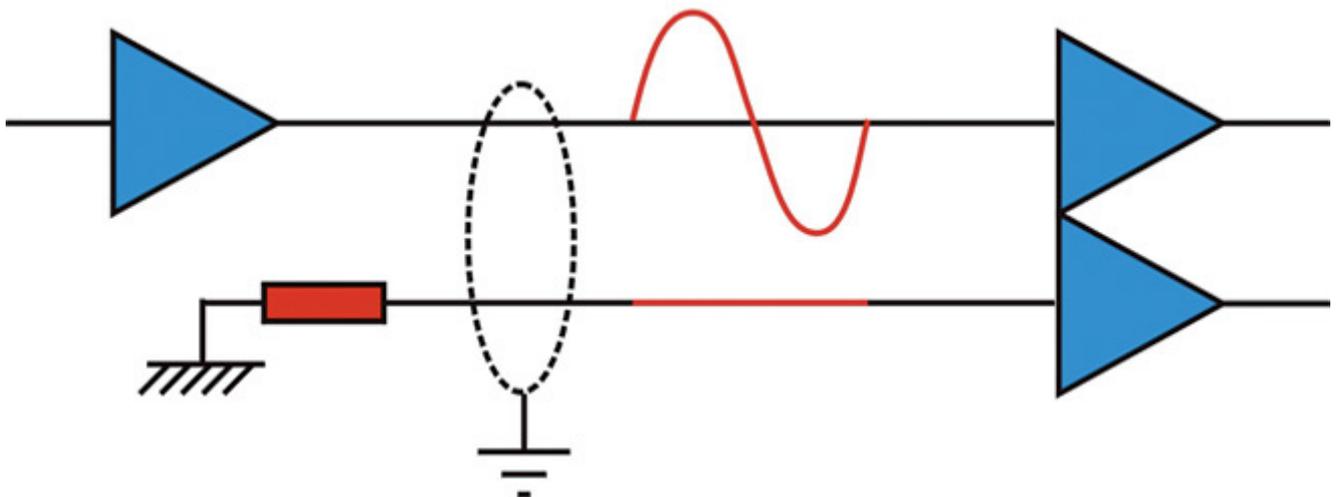
THD가

# Impedance balanced

DC

, +

-



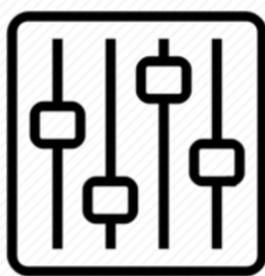
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가

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